OK TO ENTER: /F.T./

[10191/3602] UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) Jochen WEBER et al.

Serial No. 10/801,363

Filed March 15, 2004

MICROPROCESSOR SYSTEM AND METHOD FOR For

DETECTING THE EXCHANGE OF MODULES OF THE

SYSTEM

Examiner Fatoumata TRAORE

Art Unit 2436 Confirmation No. 3174

Commissioner for Patents P.O. Box 1450

Mail Stop AF

Alexandria, VA 22313-1450

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

RULE 116 RESPONSE AFTER FINAL REJECTION

SIR:

In response to the final Office Action of June 15, 2009, kindly consider the abovecaptioned application in view of the following.

Amendments to the Claims are not presented, but a listing of the claims beginning on page 2 of this paper is presented for the Examiner's convenience.

Remarks begin on page 6 of this paper.